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ternary content addressable memory coupled to arithmetic logic unit within the d


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[Content addressable memory with error detection signaling](#) - Ichino - Cited by 8

1. [A ternary content addressable search engine](#) - Solid-State Circuits ...

and the **central processing unit** (CPU). One solution to this .... lines, it is possible to **perform match operations** on arbi- trarily wide words. ...

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2. [IEEE 100 The Authoritative Dictionary of IEEE Standards Terms](#)

or through **one or more** coupler interfaces, to **perform** all of ..... a **central processing unit within** a single LSI circuit. Note: ...

[ieeexplore.ieee.org/iel5/4/116785/4116786/04116797.pdf](#) - [Similar pages](#)

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3. [APPARATUS AND METHOD FOR HIGH THROUGHPUT NETWORK SECURITY SYSTEMS ...](#)

Each of the **one or more processing** channels may be **configured** to use the ... **one** of the multitude of **processing** cores includes an **arithmetic logic unit**. ...

[www.freepatentsonline.com/y2006/0077793.html](#) - [Similar pages](#)

by T Tan - 2006

4. [Dynamic unknown L2 flooding control with MAC limits](#) - Patent 7149214

Dec 12, 2006 ... 5909686, Hardware-assisted **central processing unit** access to a forwarding ..... Often intermediate nodes employ a **content-addressable memory** ...

[www.freepatentsonline.com/7149214.html](#) - [Similar pages](#)

by MT Wu - 2006 - [Related articles](#) - [All 5 versions](#)

5. [\(WO/2007/002466\) ACCESS CONTROL LIST PROCESSOR](#)

and forwards the search key to a **ternary content addressable memory** 105 (TCAM). ..... two **arithmetic-logic units** 41215 4122 (ALU1 and ALU2) each **coupled** to ...

[www.wipo.int/pctdb/en/wo.jsp?IA=US2006024578&DISPLAY=DESC](#) - 65k

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6. [\(WO/2008/073824\) DYNAMIC PROGRAMMABLE INTELLIGENT SEARCH MEMORY](#)

The PRISM **memory** also optionally provides **ternary content addressable memory** ..... The PRISM controller, block 1203, **coupled** with the **content** stream **logic**, ...

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7. [\[PDF\] Techniques for Processing TCP/IP Flow Content in Network Switches ...](#)

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**Content Addressable Memory**, CISC, Complex Instruction Set Computer, CLB, Configurable **Logic Block**, CPU, **Central Processing Unit** ...

[cse.seas.wustl.edu/Research/FileDownload.asp?377](#) - [Similar pages](#)

8. [Cryptography - Particular Algorithmic Function Encoding inventions](#)

The calculating device is a **central unit** associated with a **memory**. .... wherein the first sub-group preferably includes **arithmetic** and/or **logic operations**, ...

[www.freshpatents.com/x138002800qpsbc.php](#) - 143k - [Cached](#) - [Similar pages](#)

9. [Ladas & Parry LLP Patents](#)

20080208949 - Distribution request control method and **unit**, and program for .....

20080080539 - Method for **ternary** contents **address memory** table management ...

[www.freshpatents.com/Ladas-Parry-LLP-andirl.php](#) - 317k - [Cached](#) - [Similar pages](#)  
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10. [\[PDF\] Advanced Synthesis Cookbook](#)

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**logic** fits within two levels of **logic**, allowing the **unit** to operate at the ..... **Ternary Content. Addressable. Memory** (TCAM), **Content Addressable Memory** ...

[www.altera.com/literature/manual/stx\\_cookbook.pdf](#) - [Similar pages](#)

11. [WASP: A WSI Associative String Processor](#)

bit-parallel vector-vector **arithmetic**, **logic** and. relational **operations**. in active APEs. for **ternary operations** such as. Z:=XopY ...

[www.springerlink.com/index/W9341M3254N62327.pdf](#) - [Similar pages](#)  
by RM Lea - 1991 - [Cited by 40](#) - [Related articles](#) - [All 2 versions](#)

12. [\[PDF\] DIADEM Deliverable 3](#)

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**content addressable memory** for Internet Protocol characterization", Field ..... o construct a pattern **matching unit** for a string, subsequently ...

[www.diadem-firewall.org/documents/Diadem%20Firewall%20-%20GD3%20-%20Attack%20Requirements%20Specification.pdf](#) - [Similar pages](#)  
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13. [\[PDF\] Intel Technology Journal](#)

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as **Ternary Content Addressable Memory** (TCAM), or. classification processors to the SRAM ..... **operations** provided by the **Arithmetic Logic Unit** (ALU). ...

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14. [\[PDF\] ILE:](#)

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made up of processors containing a few registers, an **arithmetic unit**, ..... The image **processing operations** in phase **one** are implemented via a ...

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15. [Digital Circuit Design](#)

PREFACE Digital systems are created to **perform** data **processing** and control tasks . ..... Basic Computer Architecture 59 microprocessor **Arithmetic Logic Unit ...**  
[www.scribd.com/doc/4291265/Digital-Circuit-Design](#) - 977k - [Cached](#) - [Similar pages](#)

16. [Tamil Technical Dictionary \\*\\*\\*\\*\\* ..](#)

Send to (Required, with **one** email **address** per line) ..... **arithmetic logic unit** - ±ñ¼  
¾ ²Á½ « ò, ÿñ; ALU **arithmetic operation** - ±ñ¼¾ Ä ¨É **arithmetic ...**  
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17. [/hardir/miq/a339385.tiff](#)

storage and bit-serial **processing** of **one** to three **ternary** contiguous bit fields .....  
imaging **unit** has been **configured** for. 323-002 (neo02.wp5/net)Issue 2, ...  
[www.stormingmedia.us/58/5839/A583933.pdf](#) - [Similar pages](#)  
by RM Lee - 1995 - [Related articles](#) - [All 2 versions](#)

18. [Webster's Online Dictionary - The Rosetta Edition](#)

DRAM **CONTENT ADDRESSABLE MEMORY** USING PART OF THE **CONTENT** AS  
AN **ADDRESS** · DRAM CONTROL CIRCUIT .... DRAM WITH INTEGRAL SRAM AND  
**ARITHMETIC-LOGIC UNITS ...**  
[www.websters-dictionary-online.org/browse/inventions/indexdr.html](#) - 328k  
- [Cached](#) - [Similar pages](#)

19. [MCLEAN, VA, US - Patents](#)

20090002882 DISK DRIVE **UNIT** - According to **one** embodiment a disk device .....  
KEY SELECTION DEVICE AND PROCESS FOR **CONTENT-ADDRESSABLE**  
**MEMORY** 10-23-2008 ...  
[www.lqqs.org/patents/city/us\\_va\\_mclean](#) - 178k - [Cached](#) - [Similar pages](#)

20. [ARLINGTON, VA, US - Patents](#)

20090006934 Triggering diagnostic **operations within** a data **processing** .....  
20080263115 Very long **arithmetic logic unit** for security processor 10-23-2008 ...  
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21. [\[PDF\] " Who controls the vocabulary , controls the knowledge "](#)

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**Central Processing Unit** (see CU, Dual-core Chip ) ..... **Ternary Content**  
**Addressable Memory**. TCE. Transaction Cost Economics ...  
[www.fbc-e.com/resources/acronyms/res\\_acro\\_d001\\_e.pdf](#) - [Similar pages](#)

22. [Scientific Commons: Senior Member](#)

A **Ternary Content-Addressable Memory** (TCAM) Based on 4T Static Storage  
(2003) ..... Novel radix- modulo- **arithmetic units** for residue number system ...  
[en.scientificcommons.org/senior\\_member](#) - [Similar pages](#)

23. [\[PDF\] Deploying Concurrent Applications on Heterogeneous Multiprocessors](#)

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[http://www.google.com/search?hl=en&as\\_q=ternary+content+addressable+memory...&search=&as\\_qdr=all&as\\_rights=&as\\_occt=any&or=&as\\_nlo=&as\\_nhi=&safe-images](#) (3 of 13)1/16/2009 6:48:22 PM

A software object is an abstraction of **one or more memory** locations that .....  
structions for application-specific **arithmetic and logic operations**. ...  
[www.eecs.berkeley.edu/Pubs/TechRpts/2006/EECS-2006-145.pdf](http://www.eecs.berkeley.edu/Pubs/TechRpts/2006/EECS-2006-145.pdf) - [Similar pages](#)  
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24. [\[PDF\] Untitled](#)

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In contrast to Section 2, the NNAMM's **memory unit** itself provides perfect **memory**  
trace ..... Such nodes have **one or more** incoming arcs and can have several ...  
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25. [\[PDF\] Bulletin of the Technical Committee on December 2008 Vol. 31 No. 4 ...](#)

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In the relational model, a tuple is the basic **unit of operation** and ..... or Oracle PL/  
SQL, where the demarcation between query **processing logic** and ...  
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26. [Optimal error-detecting, error-correcting and other coding and ...](#)

checkable to detect **one or more** errors. The apparatus is **configured** so ..... As such,  
computer 400 includes a **central processing unit** ...  
[www.patentstorm.us/patents/6062481/description.html](http://www.patentstorm.us/patents/6062481/description.html) - [Similar pages](#)

27. [\[PDF\] Electrical Engineering Dictionary](#)

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See **arithmetic and logic unit**. AM. See amplitude modulation. .... It has **one or**  
**more** associ- ated **processing units, memory**, and periph- ...  
[www.sigma-electronics.com/Dictionary.pdf](http://www.sigma-electronics.com/Dictionary.pdf) - [Similar pages](#)

28. [\[PDF\] Managing the Consistency of Distributed Documents Christian Nentwich](#)

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in these systems – existence of transaction **processing logic** on each ..... 1st year  
BSc/CS and MSc: no **more** than 1 optional half-unit can be non-programme ...  
[see.cs.ucl.ac.uk/pubs/phds/nentwich-thesis-final.pdf](http://see.cs.ucl.ac.uk/pubs/phds/nentwich-thesis-final.pdf) - [Similar pages](#)  
by C Nentwich - [Related articles](#) - [All 4 versions](#)

29. [\[XLS\] Glossary](#)

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418, CPU, **Central Processing Unit**, The portion of a computer system that ..... E/S  
provides additional high-speed, page-addressable **memory** used by the ...  
[www.okdhs.org/NR/rdonlyres/3F6765DF-14D1-47C5-AD01-C391FA101254/0/glossary\\_okdhs\\_03142007.xls](http://www.okdhs.org/NR/rdonlyres/3F6765DF-14D1-47C5-AD01-C391FA101254/0/glossary_okdhs_03142007.xls) - [Similar pages](#)

30. [Free On-line Dictionary of Computing FOLDOC is a searchable ...](#)

(2001-01-27) **Arithmetic and Logic Unit** <processor> (ALU or "mill") The part of the  
{**central processing unit**} which performs **operations** such as addition, ...  
[www.cs.chalmers.se/~bringer/darcs/fdo-foldoc/Dictionary](http://www.cs.chalmers.se/~bringer/darcs/fdo-foldoc/Dictionary) - [Similar pages](#)

31. [\[PDF\] N. Kumar References](#)

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accuracy **within one** bit of the encoder resolution (10"). ..... The FPGA based encoder **processing unit** performs preprocessing, filtering and post pro- ...  
[www.rti.res.in/htmls/gen/in-house/inhouse-abs-2004.pdf](http://www.rti.res.in/htmls/gen/in-house/inhouse-abs-2004.pdf) - [Similar pages](#)

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read from **memory**. Then, the **arithmetic operations** can be performed. Last, ..... 41  
shows a minimal computing system with a **central processing unit** ...  
[ftp://ftp.tik.ee.ethz.ch/pub/people/thiele/paper/diss\\_gries.pdf](http://ftp.tik.ee.ethz.ch/pub/people/thiele/paper/diss_gries.pdf) - [Similar pages](#)  
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33. [Telecommunications Glossary 2000](#)

**memory**: 1. All of the **addressable** storage space in a **processing unit** and other .....  
**One or more** transmitters that are capable of **operation** while in motion. ...  
[www.its.bldrdoc.gov/projects/dev/glossary/alidet2.html](http://www.its.bldrdoc.gov/projects/dev/glossary/alidet2.html) - [Similar pages](#)

34. [exclamation mark " double quote % percent , comma 0 zero 0/1 ...](#)

(2001-01-27) **Arithmetic** and **Logic Unit** processor (ALU or "mill") The part of the **central processing unit** which performs **operations** such as addition, ...  
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35. [Projects by Community](#)

Uses BigDecimal to handle basic **arithmetic** and **ternary** expressions. ... Libraries and programs to support **unit** and functional testing of Java UIs. ...  
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36. [\[PDF\] COMPUTER BASICS AND COMPUTER SYSTEMS](#)

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If **more** than **one** file is compared using the same criteria by **matching**, ..... **central processing unit** (CPU) and as the backbone of the computer, ...  
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37. [Tuesday.html](#)

A description of **memory operation** and various design details will be described. ....  
2MByte Flash **Memory**, 256KByte fast SRAM and a 16-bit MAC **unit**. ...  
[www.ee.iastate.edu/~marwan/mwscas96/PROGRAM/Tuesday.html](http://www.ee.iastate.edu/~marwan/mwscas96/PROGRAM/Tuesday.html) - 103k  
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38. [PhD Thesis Links](#)

In many practical situations, **one or more** desired signals need to be recovered from ..... **arithmetic operations**, tests (or branches) and **memory operations**. ...  
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39. [\[PDF\] Reference Documentation](#)

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best practices for **unit** testing). The Spring team have found that the correct ..... that **one** can **perform** some **operations** with ADO.NET **within** a consistent ...  
[springframework.net/docs/1.1.2/reference/pdf/spring-net-reference.pdf](http://springframework.net/docs/1.1.2/reference/pdf/spring-net-reference.pdf) - [Similar pages](#)

40. [TECHNICAL REPORT ABSTRACTS DATABASE # # Original design: Markus ...](#)

Second, it allows a section of text to have **more** than **one** topic at a time, ..... in use in industry as well as a pedagogical **arithmetic** and **logic unit**. ...  
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sentential **logic**, but a weaker theory than **arithmetic**, set theory, ..... Whether the **event processing** is performed in **one central unit** or in a set of ...  
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42. [Learning C# 2005 | O'Reilly Media](#)

These are comprised of **one or more** keys (referred to as a chord), ..... be able to **perform arithmetic** on instances of your fractions (add two fractions, ...  
[oreilly.com/catalog/9780596102098/toc.html](#) - 881k - [Cached](#) - [Similar pages](#)

43. [1996 NASA SBIR & STTR Abstract Archives](#)

Meals prepared from "scratch" can involve 50 to 75 or **more** sepa- rate **unit operations**. Most available small appliances are designed to **perform** only **one**, ...  
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Some 80 image **processing** algorithms were successfully implemented **within** the software architecture. In particular, the **more** complex **operation** of Feature ...  
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45. [Telecomms Terms](#)

They **perform** logical **operations** and do **arithmetic**. In computer programming, **logic** ..... Processor: Sometimes called the CPU, or **central processing unit**. ...  
[www.dcl.co.uk/surtime/glossary2.html](#) - 788k - [Cached](#) - [Similar pages](#)

46. [sultanhesari](#)

Can have **more** than **one central** idea. Can brainstorm in outline, then convert the ..... making it possible to run applications from **within** the storage **unit**. ...  
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47. [\[PDF\] ˆÖê™ëŸ™ü úÖµÖÖÖ»ÖµÖ úÖ ¿ÖÖÄÖ úßµÖ •Ö-ÖÖ»Ö](#)

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**arithmetic logic unit** execution pipeline that may include **one or more** ... Each of the **accelerator units** may be **configured to perform one or more** dedicated ...  
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48. [\[PDF\] ˆÖê™ëŸ™ü úÖµÖÖÖ»ÖµÖ úÖ ¿ÖÖÄÖ úßµÖ •Ö-ÖÖ»Ö](#)

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In addition, a label may be applied to **one or more content** items by a user or ..... The

evaluating **unit** is **configured** such that, by using a predetermined ...

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49. [BibTeX bibliography jpardistcomp.bib](#)

There was **one** volume per year, with %%% **one** to six issues, through 1989. .... digital

--- **Parallel Processing; content addressable memory; ...**

[www.math.utah.edu/pub/teX/bib/jpardistcomp.html](http://www.math.utah.edu/pub/teX/bib/jpardistcomp.html) - [Similar pages](#)

50. [\[PDF\] ICECS 2004 The 11th IEEE International Conference on Electronics ...](#)

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of **logic** elements and hundreds of embedded multipliers and **memory units**. ....

**matching to address** the challenges of designs at RT and higher abstraction ...

[www.ee.bgu.ac.il/~icesc04/Conference\\_Guide\\_and\\_Abstracts\\_Book-3.pdf](http://www.ee.bgu.ac.il/~icesc04/Conference_Guide_and_Abstracts_Book-3.pdf)

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51. [DoD SBIR FY04.1 - SOLICITATION SELECTIONS w/ ABSTRACTS](#)

In addition, **one or more** new, advanced algorithms for retrieving detailed .... for a

large integer processor (LIP) able to **perform arithmetic operations**, ...

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**one or more** compositions infinitely many times. SERA's. implementation, the .....

**operations** that a **unit** may **perform** during execution affect its syn- ...

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by **one or more operation**. Figure 6.5 shows that the **operation** stretch k can be .....

machine would not be **configured** properly. Thus, **unit** testing is very ...

[www.cas.mcmaster.ca/esq/papers/SQRI/report34.pdf](http://www.cas.mcmaster.ca/esq/papers/SQRI/report34.pdf) - [Similar pages](#)

54. [Stay Hungry. Stay Foolish - Blog](#)

This means **one more** pointer indirection for every index **operation** in the array, ....

**One** benefit of **unit** testing is that it helps you develop your code ...

[algorithm.com.au/blog/files/category-2026on-coding.php](http://algorithm.com.au/blog/files/category-2026on-coding.php) - 401k

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55. [\[PDF\] Information Technology Security Evaluation Manual \(ITSEM\) Version 1.0](#)

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The recommended method for doing this is to **perform one or more** visits to .....

There is a basic crypto **unit** which can be **configured** to be a HCU or a TCU ...

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56. [\[PDF\] Rule-based Compilation](#)

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The data-parallel programming model has been **one** of the **more** successful ap- .....

time performance penalties, the compilation system must **perform within ...**

[www.pds.ewi.tueft.nl/pubs/ph\\_d/breebaart.pdf](http://www.pds.ewi.tueft.nl/pubs/ph_d/breebaart.pdf) - [Similar pages](#)

57. [\[PDF\] 3rd Workshop on Ontologies in Agent Systems](#)

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Each quantity belongs to exactly **one** dimension. A **unit** is ..... select and **perform operations** without violating the consistency ...

[ebiquity.umbc.edu/get/a/publication/181.pdf](http://ebiquity.umbc.edu/get/a/publication/181.pdf) - [Similar pages](#)

by S Melbourne - [All 3 versions](#)

58. [\[PDF\] The PowerPC Compiler Writer's Guide](#)

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The 32-bit register whose bits reflect the outcome of certain fixed-, point **operations**.

Fixed-Point **Unit**. A **logic** block that executes integer **arithmetic** and ...

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There is a basic crypto **unit** which can be **configured** to be a HCU or a TCU ..... is either 0 or 1) rather than **ternary logic** (where the output of the gate ...

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60. [View All Technical Reports: UNM Computer Science](#)

The aggregation can be a simple **arithmetic operation** or a **more** complex ..... sector

graph model vertices are placed uniformly at random in the **unit** square. ...

[www.cs.unm.edu/research/tech-reports/view\\_all\\_technical\\_reports/](http://www.cs.unm.edu/research/tech-reports/view_all_technical_reports/) - 451k

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61. [RubyBook](#)

**More file operations** The File class also supports a number of other file ..... **UNIT**

TESTING Test Driven Development is the new hotness, especially in the ...

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62. [\[PDF\] SEMI International Standards: Compilation of Terms](#)

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components that together **perform one or more** functions is represented by a

single ..... uniquely **addressable unit** of information **within** the document; ...

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63. [\[PDF\] ABSTRACT Title of Dissertation: TOWARDS THE SEMANTIC WEB ...](#)

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plicitly commit to **one or more** autonomously developed ontologies. This technique

permits ..... quence of **operations** are treated as a **unit**. ...

[www.cse.lehigh.edu/~hedin/pubs/hedin-thesis.pdf](http://www.cse.lehigh.edu/~hedin/pubs/hedin-thesis.pdf) - [Similar pages](#)

64. [Technical Resources](#)

When you start writing **unit** tests, you try to make your classes **more unit ...** of six

**units** of study (Boolean Functions and Computer **Arithmetic; Logic**; ...

[www.google.com/notebook/public/11921701893753068641/BDReplgoOutqWvqEj](http://www.google.com/notebook/public/11921701893753068641/BDReplgoOutqWvqEj) -



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65. [\[PDF\] SCALABLE COMPUTING Practice and Experience](#)

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GRIS executes (and creates the processes for) **one or more** affected ..... the **unit** of computation. It is a sequential flow of control **within** a single ...

[www.scpe.org/vols/vol06/SCPE\\_6\\_1.pdf](http://www.scpe.org/vols/vol06/SCPE_6_1.pdf) - [Similar pages](#)

66. [Cover Pages: XML Articles and Papers. January - March 2002.](#)

Of course, a message could include **one or more** documents, ..... **Within** a near decomposable **unit**, it is important to reduce complexity in data sharing. ...

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67. [\[PDF\] ABSTRACT Title of Dissertation: TOWARDS THE SEMANTIC WEB ...](#)

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to extract **content** and **perform** automated **processing** on the documents. .... are used to ensure that a sequence of **operations** are treated as a **unit**. For ...

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68. [\[PDF\] Technical Question Bank](#)

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between the main **memory** and the CPU (center **processing unit**). ..... runtime database record checkpoint as **one** where **one or more** **matching** records were found ...

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69. [\[PDF\] Teradata RDBMS SQL Reference Volume 2 - Statement and Transaction ...](#)

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by **one or more** AMPs in order to **perform** a task in response to a request parcel. .... Loosely defined, a query block is a **unit** for which the Optimizer ...

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70. [\[PDF\] Algebraic Process Calculi: The First Twenty Five Years and Beyond ...](#)

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each action occurring **within** a node takes **one** time **unit**. ..... Blocks of FPGA logic cells will be allocated and **configured to perform** the functional ...

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71. [\[PDF\] epi1b fly.pmd](#)

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Includes **content addressable memory**. ..... **one or more** loads. Covers, for example, switch control ... Power supply **unit operation** within a portable ...

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72. [\[PDF\] The Unified Modeling Language Reference Manual](#)

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and **one or more** actors of the system **unit**. The purpose of a use case is to .....

**perform operations.** It is similar to an object diagram that shows the ...  
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73. [\[PDF\] Volume 32 Number 1 April 2008](#)

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**arithmetic** and **address** operator have been removed). On. the producer side of the  
prototype ..... are connected to a video **processing unit** to extract high- ...  
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74. [false false false Baq false false false false false false ...](#)

This class represents a generic and instantiable **unit** of work, .... It serves as an  
aggregation point to associate **one or more** of the following elements: ...  
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75. [\[PDF\] LAC2005 Proceedings](#)

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or parallel, and similar **operations** that involve the. spawning of **unit** generators. ....  
gramming: it compares the **content** of a **memory address** ...  
lac.zim.de/2005/papers/lac2005\_proceedings.pdf - [Similar pages](#)

76. [\[PDF\] Mathematical Journals as Reasoning Agents: Literature Review Contents](#)

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Once constructed, the user can do various **operations within**. this ring. There are  
ways to use **more than one** ring at once. Use S:=Z/(5)[a,b,c]; F:=a-b; ...  
www.risc.uni-linz.ac.at/publications/download/risc\_3442/Math-Agents-for-SFB-2008-03-  
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77. [\[XLS\] Cart 1](#)

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13.3.1 **Arithmetic Logic Unit (ALU)**. 13.3.2 Register File. 13.3.3 Control **Unit**. ...  
15.1.1 **Central Processing Unit**. 15.1.2 **Memory**. 15.1.3 Input/Output Ports. ...  
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method allows you to push **one or more** elements onto the end of an array; ..... be  
sure to check out the Test::Unit documentation at http://www.ruby- ...  
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79. [\[PDF\] Undergraduate Prospectus 2006-2007](#)

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instructions, **Arithmetic** and **Logic Unit**. Basic ... machine level: **Central Processing**  
**Unit (CPU)**,. **memory**, and Input/Output. Interfacing CPU ...  
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carry out the design technology research in the context of **one or more** real ..... entire program into a single compilation **unit**. To manage compile time, ...  
[www.gigascale.org/pubs/reports/annual/AnnualReportFinal2000.pdf](http://www.gigascale.org/pubs/reports/annual/AnnualReportFinal2000.pdf) - [Similar pages](#)

81. [\[PDF\] EBU Tech 3274-2 - 1998 EBU Glossary, Part3](#)

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CPU, microprocessor/**central processing unit**. **Central** component of a computer, ..... A stream of all the digital data carrying **one or more** services **within** a ...  
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82. [\[PDF\] EPI FLY PART 2.pmd](#)

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**one or more** loads. Covers, for example, switch control. to distribute power where required. ... This code covers **operation** of a power supply **unit within** ...  
[scientific.thomsonreuters.com/m/pdfs/epi\\_manualcodes2.pdf](http://scientific.thomsonreuters.com/m/pdfs/epi_manualcodes2.pdf) - [Similar pages](#)

83. [Haskell Communities and Activities Report](#)

Nov 5, 2008 ... Therefore, the performance of **arithmetic operations** is ..... We are a two-man **unit** of functional programming research **within** the ...  
[www.haskell.org/communities/11-2006/html/report.html](http://www.haskell.org/communities/11-2006/html/report.html) - 424k  
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84. [\[PDF\] Laboratory Directed Research and Development Program](#)

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**Memory** (PIM) design for the **central processing units**. (CPU). **One** of our first projects was the evaluation of two. related processors that are designed to ...  
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single **processing unit**, its task can be parallelized by executing the detection ..... the **operations** are listed in the order of the number of **arithmetic** ...  
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**Unit operations** (symbols) and **unit** processes, Batch and continuous **processing**, ..... only and **content addressable memory**. RAM - Static & dynamic. ....  
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furthermore be tuned by changing **one or more** of the anisotropy field, the emagnetizing ..... **arithmetic operations** by manipulating the single-electrons. ...  
[delft01.acae.cuhk.edu.hk/eee-nano2007/homesubmenu/eee-nano-2007-technical%20digest-full-version-reduced.pdf](http://delft01.acae.cuhk.edu.hk/eee-nano2007/homesubmenu/eee-nano-2007-technical%20digest-full-version-reduced.pdf) - [Similar pages](#)

88. [@INPROCEEDINGS/0410:Quality. TITLE="Quality Effects of wireless ...](#)

Cost Functions: The effective cost per **unit** of energy for each of the proposed ... based on a new **Ternary Content Addressable Memory** configuration. ...

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89. [Walker, Gordon James Conceptual Development and Empirical Testing ...](#)

The performance and the **operation** principles of **one**-choke and two-choke ... And finally, in large volumes, FPGAs tend to be **more** expensive per **unit** due to ...

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that a processor and **memory** pair is connected to **one or more** other processor and ..... **within** two neighbourhood of the **central processing** element). ...

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91. [0060006 SBIR Phase I: Estimating Software Costs for Web-Enabled ...](#)

In Phase II, a film analysis **unit** will be mated to an existing pilot production ..... of **ternary** alloy growth necessitates many **more** reactions and species. ...

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92. [%T Performance Estimates for the DADO Machine: A Comparison of ...](#)

The basic **unit** of computation is potentially useful in checkpointing, ..... **content-addressable memory**, database computer, **logic**-per-track, mass **memory**, ...

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93. [\[PDF\] AMD Geode GX1 Processor Data Book](#)

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Branch prediction **logic** **within** the prefetch **unit** generates a ..... asserted followed by **one or more** data phases. During the **address** phase, ...

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Membership in **one or more** of 39 IEEE Societies and three Technical Councils spanning ..... A Study on Constructing the **Arithmetic Unit** over GF(3m) ...

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**processing logic** of the system and when future changes in func- ..... module calls but trampolines to jump from **one** compilation **unit**. to another. ...

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Because **one** of the **processing units** performs a computation intensive job, .....  
**arithmetic**-geometric mean difference in terms of certain **central** moments. ...  
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98. [\[PDF\] GRID RESOURCE MANAGEMENT](#)

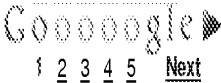
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Pegasus **configured to perform** the LIGO pulsar search, based on application-  
specific metadata. .... requires in each mode exactly **one resource unit**: ...  
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100. [AAAI-06 Technical Paper Abstracts Tuesday, July 18](#)

When this approach is used to solve graph-search problems with **unit** edge costs, ...  
shown that a breadth-first search strategy can be **more memory**-efficient ...  
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ternary content addressable memory coupled to arithmetic logic unit within the c

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